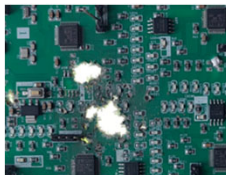
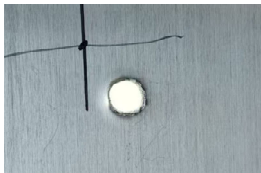


1st-layer plate2nd-layer circuit board3rd-layer plate

(a)

948 ms⁻¹

(b)

1012 ms⁻¹

(c)

984 ms⁻¹